

IN THE SPECIFICATION

Please replace the paragraph at page 1, line 4 to line 14, with the following:

Cross Reference To Related Applications

The present application is a continuation of U.S. Application No. 09/891,699 filed June 26, 2001 entitled "Method and Apparatus for Self-Referenced Wafer Stage Positional Error Mapping", by Adlai Smith, Bruce McArthur, and Robert Hunter, Jr., which claims priority to U.S. Provisional Application Serial No. 60/254,413 filed December 8, 2000 entitled "Method and Apparatus for Self-Referenced Wafer Stage Positional Error Mapping", by Adlai Smith, Bruce McArthur, and Robert Hunter, Jr. These applications are incorporated by reference in their entirety herein.